



2012 Technological Studies

Advanced Higher

Finalised Marking Instructions

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Section A

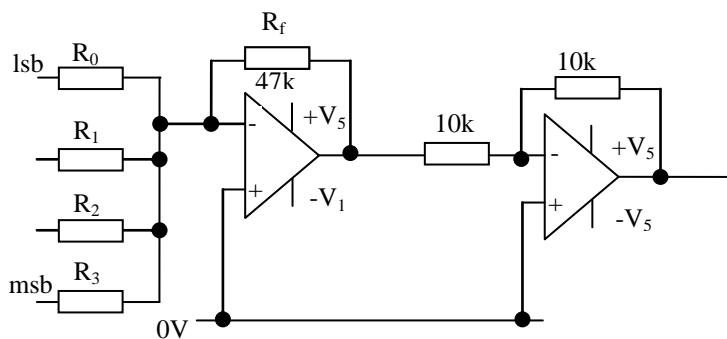
Q1

- (a) • Clock provides regular digital pulses.
- AND gate allows pulses through to binary counter (when comparator is high)
- binary counter counts pulses in a binary form.
- Digital to Analogue Convertor (DAC) converts binary value into equivalent analogue value for comparator.
- Comparator compares analogue temp signal with signal from DAC. Output is digital which controls AND gate.
- BCD-7 seg decodes 4-bit binary into a format suitable for 7-seg display.
- 7-seg displays freezes at decimal value (0-9) equivalent to temperature signal.

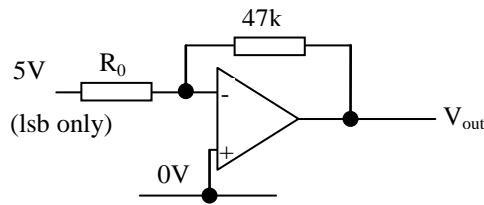
Marks

1	1	1	1	1	1	1	7
---	---	---	---	---	---	---	---

(b)



2



$$2^4 = 16 \quad (0 \rightarrow 15)$$

1

$$V_{\text{out}} = \frac{1}{15} \times -6 = -0.4 \text{ V}_{\text{resolution}}$$

1

$$V_{\text{out}} = \frac{-R_f}{R_o} \times V_{\text{in}}$$

1

$$-0.4 = \frac{-47}{R_o} \times 5$$

1

$$R_o = \frac{47}{0.4} \times 5 = 587.5 = 588 \text{ k}\Omega$$

1

$$\left. \begin{array}{l} R_1 = 294 \text{ k}\Omega \\ R_2 = 147 \text{ k}\Omega \\ R_3 = 73.5 \text{ k}\Omega \end{array} \right\}$$

1

8

(15)

Marks	
1	
1	
1	
1	4
2	
1	
2	5
2	
1	3
	(12)

Q2

(a) $I = \frac{\pi}{64} (D^4 - d^4)$

$$= \frac{\pi}{64} (30^4 - 27^4)$$

$$= \frac{\pi}{64} (278559)$$

$$= 13673.7 \text{ mm}^4$$

(b) $M = \frac{FL}{4} = \frac{200 \times 1000}{4} = 50000 \text{ Nmm}$

Substitutions, answer

$$\sigma = \frac{My}{I} \quad y = 15 \text{ mm}$$

Answer

$$\sigma = \frac{50000 \times 15}{13673.7} = 54.8 \text{ N/mm}^2$$

Substitutions, answer

(c) $\delta = \frac{FL^3}{48EI} \quad E = 150 \times 10^3 \text{ N/mm}^2$

$$\delta = \frac{200 \times 1000^3}{48 \times 150 \times 10^3 \times 13673.7}$$

Substitutions, answers

$$\delta = 2.03 \text{ mm}$$

Q3

	Software	Hardware	Marks
Write the assembler code	Text editor		
Convert assembler into machine code	MPASM assembler		
Burn machine code into microcontroller	Download program	Programmer	
Test in target system	Test program	target	8
Any 4 points from each column			(8)

Marks	
	2
6	
3	
1	
3	
1	
8	
	(16)

Q4

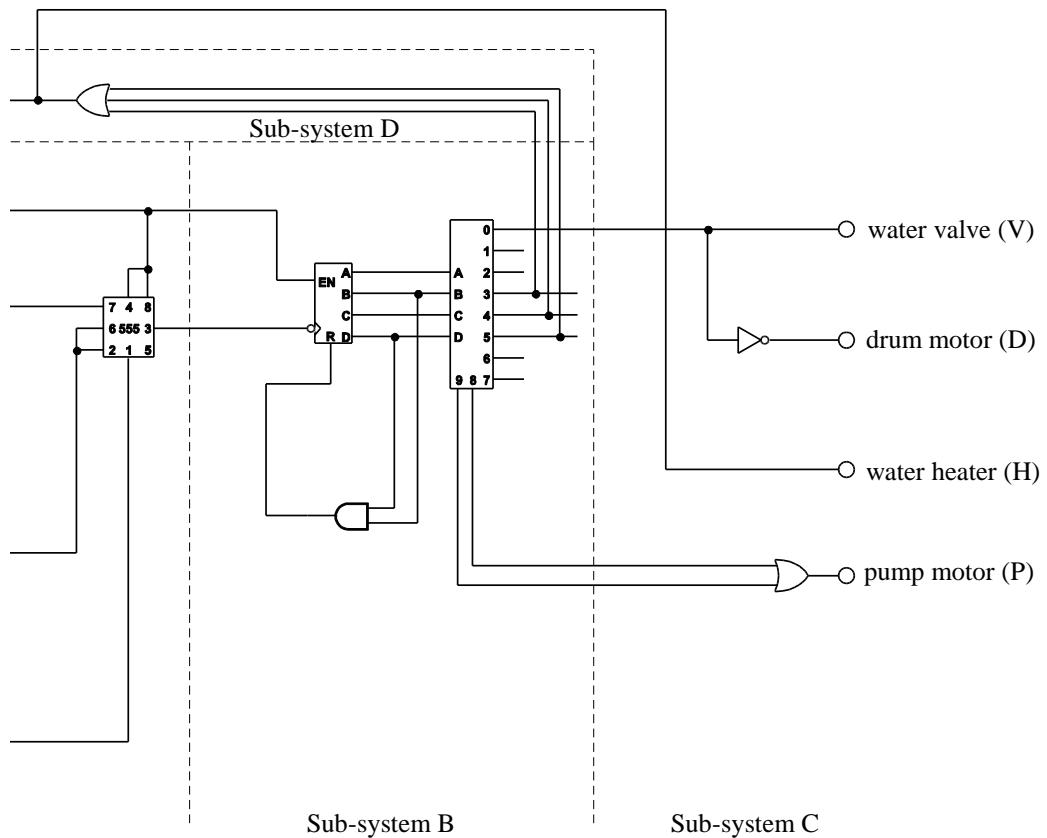
(a) $H = 3 + 4 + 5$

(b) See Worksheet Q4 below

- (c)
- 555 is an astable to provide regular digital clock pulses whose frequency is determined by R_1 & R_2 & C
 - relay is operated by OR gate which switches between $10M\Omega$ & $1M\Omega$ resistors
 - changing frequency of astable by a factor of 10
 - OR is only high during C_3 or C_4 or C_5
 - Therefore water heater is on 10 times longer than duration of these clock pulses
 - 4-bit binary counter counts astable pulses and resets on 10 via AND gate
 - BCD – Decimal decoder decodes binary value of count into a decimal output
 - logic array interprets decimal outputs for washing machine outputs

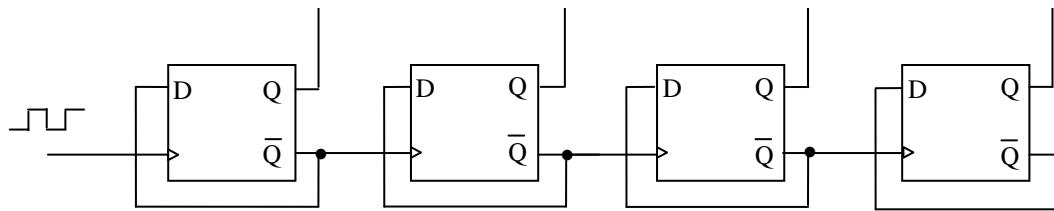
Sub-system A
Sub-system D
Sub-system B
Sub-system C

Worksheet Q4

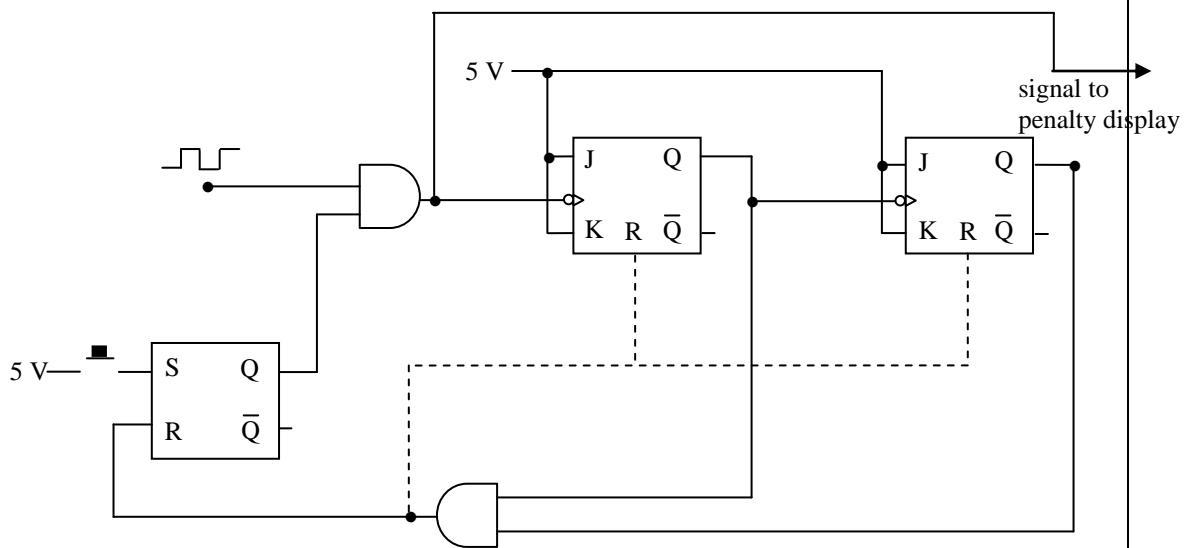


Marks	
	3
	9
	3
	(15)

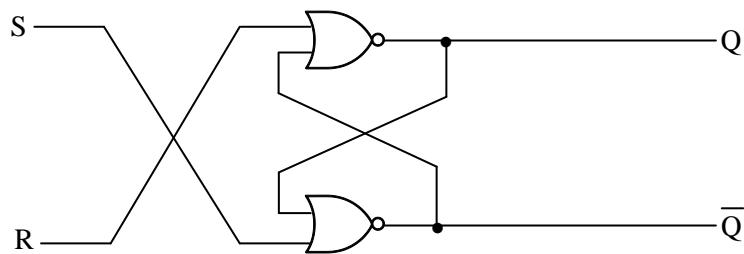
Q6 (a)



(b)



(c)



				Marks
Q7				
(a)	main:	call adcread ; value in DATA movfw DATA ; value now in W sublw d'128' ; 128 - W (128 - value) movwf ERROR ; error now in W btfsf STATUS, Z ; check for zero error goto main ; yes so loop back btfsf STATUS, C ; no, check for neg error goto jumpneg	1 1 1 1 1 1 1 1	
		call sluiceup goto main }	1	
	jumpneg:	call sluicedown goto main }	1	10
(b)	sluiceup:	movlw b'1100 0000' movwf PORTB movlw d'100' call wait } movlw b'01100000' movwf PORTB } movlw d'100' call wait } movlw b'00110000' movwf PORTB } movlw d'100' call wait } movlw b'10010000' movlw d'100' call wait } return	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	6
				(16)

Q8

		Marks
(a)	$\sum M_B = 0$ $+ (R_A \times 4) - (6 \times 1) - (3 \times 4 \times 2) = 0$ $+ 4R_A = 6 + 24 = 30$ $R_A = +7.5 \text{ KN} \uparrow$	1 1 1 1 3
(b)	$M = + (R_a \times x) - \left(\omega x \times \frac{x}{2} \right)$ $= + 7.5x - \frac{3}{2}x^2$ $M = -1.5x^2 + 7.5x$	1 1 1 3
(c)	$\frac{dM}{dx} = -3x + 7.5 = 0$ $x = \frac{7.5}{3} = 2.5 \text{ m}$ from LHS	2 1 3
(d)	$M = -1.5x^2 + 7.5x$ $= -(1.5 \times 2.5^2) + (7.5 \times 2.5)$ $= -9.375 + 18.75$ $= +9.375 \text{ kNm}$	2 1 3
		(12)

Marks	
	1
1	
1	
1	3
1	
1	
1	7
1	1
1	
1	
1	
1	
1	
1	
1	
1	
1	
1	
1	5
1	

Q9

(a) Wein Bridge

$$(b) \quad f = \frac{1}{2\pi RC}$$

$$50 = \frac{1}{2 \times \pi \times R \times 4.7 \times 10^{-6}}$$

$$R = \frac{1}{2 \times \pi \times 50 \times 4.7 \times 10^{-6}}$$

$$R = 677 \Omega$$

(c) A&B are Schmitt Triggers to convert the sine waves to digital square waves
C = 4 bit binary counters

1st stage counts to 10 & resets

2nd stage counts to 5

\therefore counts to 50

counts are only enabled via AND gates when NOR gate is high

∴ first circuit to count to 50 disables further counting

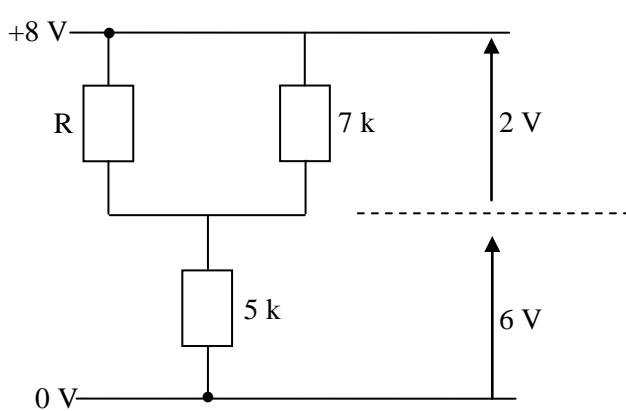
If both circuits reach 50 at same time = freq correct indicator

If both circuits reach 50 at same time = freq cor

If bottom circuit first to 50 = freq too high indicator

(d) (i)

(ii)



$$\frac{R_p}{5} = \frac{2}{6}$$

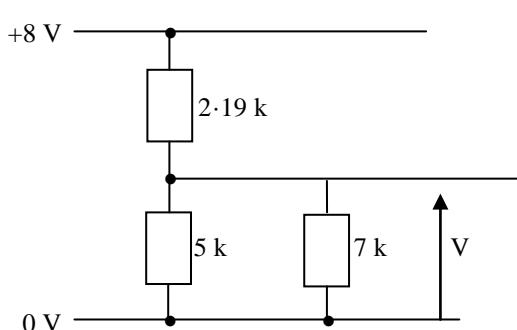
$$R_p = \frac{5 \times 2}{6} = 1.667 k\Omega$$

$$1 \cdot 667 = \frac{R \times 7}{R + 7}$$

$$\begin{aligned} 1 \cdot 667(R + 7) &= 7R \\ 1 \cdot 667R + 11 \cdot 669 &= 7R \\ 5 \cdot 333R &= 11 \cdot 669 \\ R &= 2 \cdot 19k\Omega \end{aligned}$$

Q9 (d) (continued)

(iii)



$$R_P = \frac{5 \times 7}{5 + 7} = \frac{35}{12} = 2.92\text{k}\Omega$$

$$V = \frac{2 \cdot 92}{5 \cdot 11} \times 8$$

$$V = 4 \cdot 57V$$

(Switch on threshold)

(e) main: clrf COUNT }
 clrf EDGE

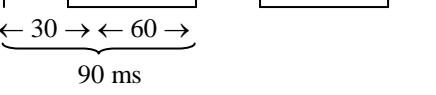
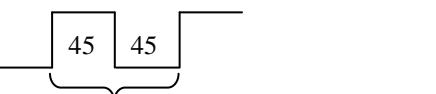
loop:	btfsc	PORTB, 1
	goto	jump
	clrf	EDGE
	goto	loop

jump:	movlw	d'1'
	xorwf	EDGE, W
	btfsc	STATUS, Z
	goto	loop
	incf	COUNT, F
	incf	EDGE, F
	btfss	PORTB, 0
	goto	loop
	movlw	d'50'
	subwf	COUNT
	btfss	STATUS, Z
	goto	jump2
	bsf	PORTB, 5

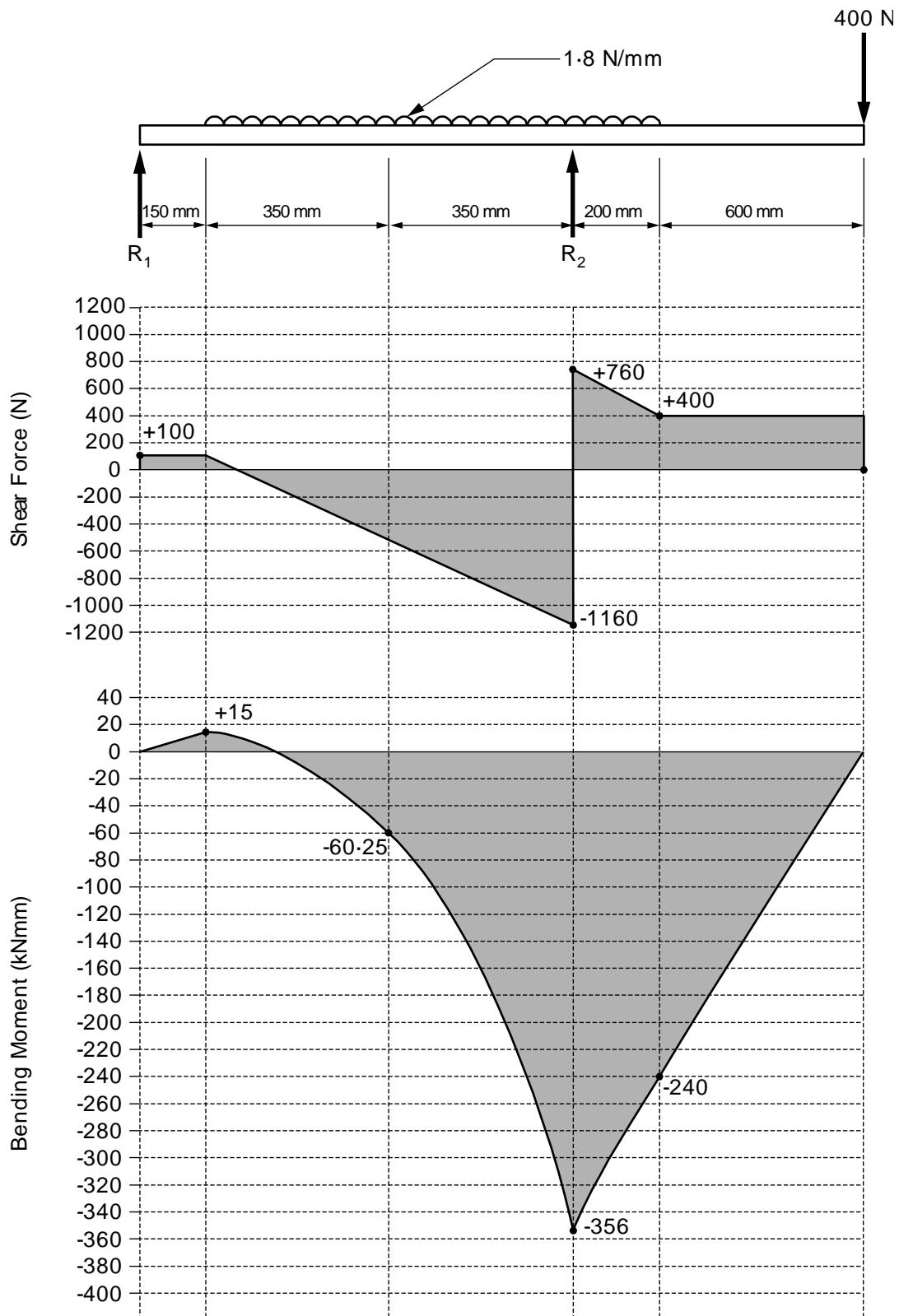
delay:	movlw	d'100'	{}
	call	wait	
	bcf	PORTB, 7	
	bcf	PORTB, 6	
	bcf	PORTB, 5	
	goto	main	

```
jump2:    btfss    STATUS, C
           goto    jump3
           bsf     PORTB, 6
           goto    delay }
```

```
jump3:    bsf      PORTB, 7  }
           goto    delay
```

			Marks	
Q10				
(a)	(i)		30 : 60 $\frac{12 \cdot 6}{0 \cdot 09} = 140 \text{ cycles}$	1 1 2
	(ii)		$\frac{16 \cdot 2}{0 \cdot 09} = 180 \text{ cycles}$	1
(b)	main:	clrf SOLO	1	
	loop:	btfss PORTB,0	1	
		goto loop	(with 2 nd "gotoloop" below) 1	
		incf SOLO, F	1	
		movlw d'200'	1	
		call pause } btfsc PORTB,0	1	
		incf SOLO, F	1	
		clrw	1	
		xorwf SOLO, W	1	
		btfsc STATUS, Z	1	
		goto loop		
		movlw d '1'	1	
		xorwf SOLO, W	1	
		btfss STATUS, Z	1	
		goto jump	1	
		call mode 1	1	
	jump:	call mode 2	1	
		goto main	1 17	
(c)	$\Sigma M_{RI} = 0$ $+ (1.8 \times 900 \times 600) + (400 \times 1650) - (R_2 \times 850) = 0$ $+ 972,000 + 660,000 = 850R_2$ $R_2 = +1920N \uparrow$			
	$\Sigma F_V = 0$ $+ R_1 - (1.8 \times 900) - 400 + 1920 = 0$ $+ R_1 - 1620 - 400 + 1920 = 0$ $R_1 = 100 N \uparrow$			
(d)	See Worksheet Q10			
	4 Shear-Force values at 1 each			
			4	

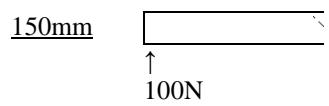
Worksheet Q10



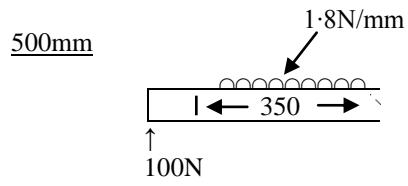
Marks	
1	
2	
2	
2	
2	
1	
2	
12	
	(40)

Q10 (continued)

(e) Omm $M = 0 \text{ Nmm}$



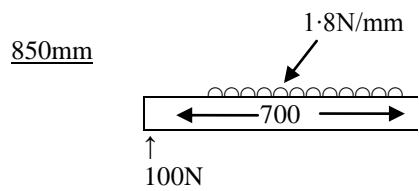
$$M = + (100 \times 150) = +15,000 \text{ Nmm}$$



$$M = + (100 \times 500) - \left(1.8 \times 350 \times \frac{350}{2} \right)$$

$$M = + 50,000 - 110,250$$

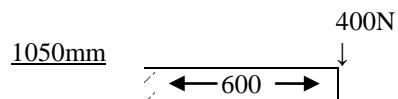
$$M = - 60,250 \text{ Nmm}$$



$$M = + (100 \times 850) - \left(1.8 \times 700 \times \frac{700}{2} \right)$$

$$M = + 85,000 - 441,000$$

$$M = - 356,000 \text{ Nmm}$$



$$M = - (400 \times 600) = - 240,000 \text{ Nmm}$$

1650mm $M = 0 \text{ Nmm}$

Plot of B-M diagram: points (1)
 shape (1)

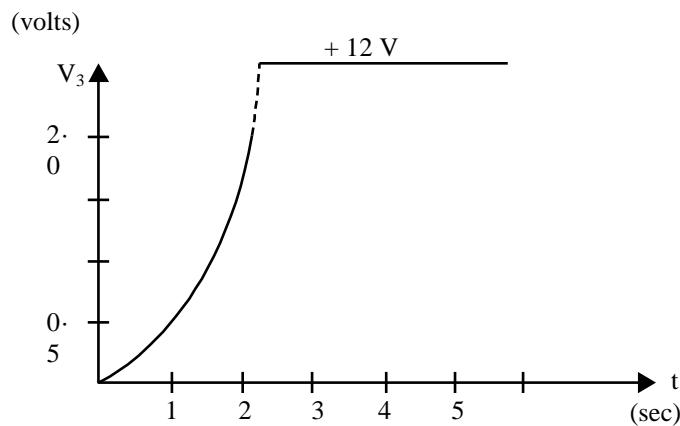
Marks	
	1
1	
1	
1	
1	
1	5
5	

Q11

(a) (i) $V_2 = -t$

(ii)
$$\begin{aligned} V_3 &= -\frac{1}{RC} \int V_2 dt \\ &= -\frac{1}{1 \times 10^6 \times 1 \times 10^{-6}} \int (-t) dt \\ &= +\frac{1}{1} \times \frac{t^2}{2} \\ V_3 &= +\frac{1}{2} t^2 \end{aligned}$$

(iii)



(iv) $V_2 = -\frac{1}{RC} \int V_1 dt$

$$-t = \frac{1}{560 \times 10^3 \times 4.7 \times 10^{-6}} \int V_1 dt$$

$$-t = -\frac{1}{2.632} \times \frac{V_1 t^1}{1}$$

$$1 = \frac{V_1}{2.632}$$

$$V_1 = 2.63 \text{ V}$$

Marks	
	1
1	
1	
1	
1	
1	
1	
1	7
1	
1	
1	
1	
1	
1	
1	3

Q11 (continued)

- (b) (i) Voltage Controlled Oscillator
(ii) V_{ref} is provided by voltage divider

V_1 charges up capacitor via input resistor

The comparator switches on relay (via transistor) when capacitor voltage
 $> V_{ref}$.

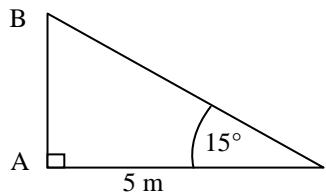
Relay briefly switches on buzzer and discharges capacitor.

Cycle repeats.

Magnitude of V_1 affects frequency of buzzing.

The greater the acceleration, the greater the frequency.

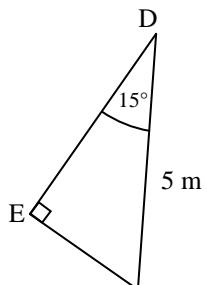
(c)



$$\tan 15^\circ = \frac{AB}{5}$$

$$AB = 5 \tan 15^\circ = 1.34 \text{ m}$$

$$BC = 5 - 1.34 = 3.66 \text{ m}$$



$$\cos 15^\circ = \frac{DE}{5}$$

$$DE = 5 \cos 15^\circ = 4.83 \text{ m}$$

Marks	
1	1
2	
1	1
2	
1	1
2	13
	(40)

Q11 (continued)

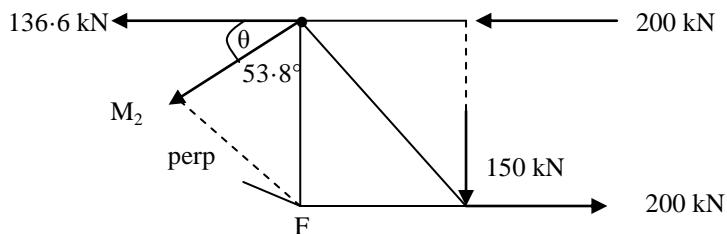
$$(d) \quad \Sigma M_D = 0 + \downarrow \\ + (M_3 \times 4.83) + (150 \times 5) - (200 \times 5) = 0 \\ + 14.83 M_3 + 750 - 1000 = 0 \\ M_3 = \frac{250}{4.83} = 51.8 \text{kN} \quad (\text{TIE})$$

$$\Sigma M_B = 0 + \curvearrowleft$$

$$-(M_1 \times 3.66) + (150 \times 10) - (200 \times 3.66) - (200 \times 1.34) = 0$$

$$-3.66M_1 + 1500 - 732 - 268 = 0$$

$$M_1 = \frac{500}{3.66} = +137\text{kN} \quad (\text{TIE})$$



$$\begin{aligned}\Sigma F_V &= 0 \left[\begin{array}{c} \uparrow \\ + \end{array} \right] \\ +M_3 \cos 75 - M_2 \cos 53.8 - 150 &= 0 \\ M_2 \cos 53.8 &= M_3 \cos 75 - 150 \\ M_2 &= \frac{51.8 \cos 75 - 150}{\cos 53.8} \\ M_2 &= \frac{13.4 - 150}{\cos 53.8} = -233 \text{ kN} \quad (\text{STRUT})\end{aligned}$$

[END OF MARKING INSTRUCTIONS]