

Fourth Semester LL.M. Examination, December 2013 LAW OF INTELLECTUAL PROPERTY AND INFORMATION TECHNOLOGY LAW (IPR Branch) (Specialisation Paper – VIII) (Course – II)

Duration: 3 Hours Max. Marks: 80

Instructions: 1. Answer all five questions.

- 2. Answer **one** essay type question and **one** short note question from **each** Unit.
- 3. Figures to the **right** indicate **marks**.

UNIT - I

Q. No. 1. (a) Discuss the remedies available in case of copyright infringement and procedure there on.

Marks: 10

OR

OR

Examine the nature and scope of rights in designs under the Designs Act 2000.

(b) Write short notes on:

Marks: 6

e-commerce.

Cyber crimes.

UNIT - II

Q. No. 2. (a) Explain the provisions of IT Act 2000 relating to secure electronic records and secure digital signatures.

Marks: 10

OR

Discuss the important changes brought about by the Information Technology Amendment Act, 2008.

(b) Write short notes on:

Marks: 6

Acknowledgment of receipt.

OR

Time and place of dispatch and receipt of electronic record.

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Q. No. 3.(a) Explain the provisions of IT Act relating to digital signature certificates.

OR

Examine the provisions relating to admissibility of electronic records as evidence.

(b) Write short notes on:

Marks: 6

Duties of subscribers.

OR

Power of the police to investigation search and arrest.

UNIT - IV

Q. No. 4.(a) Bring an account of relationship between Information Technology and Patents Act, 1970.

Marks: 10

OR

Discuss the issues relating to infringement of trade marks in the electronic medium.

(b) Write short notes on:

Marks: 6

Scope of copy right in cinematograph film.

OR

Infringement of designs in electronic form.

UNIT - V

Q. No. 5. (a) Discuss the objectives, scope and structure of UNICITRAL model law.

Marks: 10

OR

Discuss the provisions of DMCA relating to on-line copy right infringement liability limitation.

(b) Write short notes on:

Marks: 6

Communication of data messages under UNICITRAL model law.

OR

Salient features of Treaty on Intellectual Property Rights in respect of integrated circuits.